Avalanche Technology Introduces Two New Evaluation Kits to Enable Industry 4.0 Applications

The New Kits Enable Designers to Quickly Evaluate Avalanche’s Highly Reliable Non-Volatile Spin Transfer Torque MRAM in Their Industrial Automation and IoT Designs

FREMONT, Calif. (PRWEB) October 12, 2020 -- Avalanche Technology, the leader in next generation MRAM technology, announced today the immediate availability of two new development kits, the Avalanche P-SRAM (Persistent SRAM) Serial QSPI Evaluation Kit and the Avalanche P-SRAM Parallel Interface Evaluation Kit. These new kits are rapid prototyping platforms consisting of 1) a host board, and 2) a UNO R3 form factor daughter card with Avalanche P-SRAM, which designers can use to quickly develop Industry 4.0 applications that require data logging or program storage.

The Avalanche P-SRAM Serial QSPI evaluation kit enables system designers to evaluate Avalanche's Serial High Performance P-SRAM using a STMicro Nucleo motherboard connected to an Avalanche daughter card via the standard QSPI interface. The STMicro NUCLEO-H743ZI2 motherboard communicates with the computer using a USB 2.0 cable type A/B and terminal emulator software using Avalanche’s easy-to-use software application. The Avalanche daughter card is populated with an AS3016204 high-performance P-SRAM device. Avalanche’s high-performance P-SRAM is STT-MRAM (Spin Transfer Torque MRAM) offered in 1Mb to 16Mb density options, and is a true non-volatile random-access memory, ideal for applications that must store and retrieve data without incurring large latency penalties. These devices offer low power, and virtually infinite endurance and retention. They are available in standard small-footprint packages that are ideal for form-factor-constrained IoT applications as well as Industry 4.0 applications for non-volatile memories.

The Avalanche P-SRAM Parallel Interface Evaluation Kit is based on the STMicro NUCLEO-F746ZG motherboard that communicates with the included Avalanche daughter card through a standard asynchronous SRAM interface. The STMicro Nucleo-F746ZG motherboard communicates with the computer using a USB 2.0 cable type A/B and terminal emulator software using Avalanche’s easy-to-use software application. The Avalanche daughter card is populated with the AS3016316-035 P-SRAM device. The Avalanche Parallel P-SRAM is also based on the same highly reliable, non-volatile STT-MRAM technology and is available in 1Mb-32Mb density options. These devices come with a standard 35-nanosecond asynchronous SRAM interface and offer low power, and virtually infinite endurance and retention. These devices are ideal for non-volatile memory applications such as program storage and data logging in Factory Automation, Motor Control, Human Machine Interfaces, Smart Meters, and Multifunction Printers.

More information about the new evaluation kits is available at https://www.avalanche-technology.com/support/development-kits/.


Availability

Avalanche’s P-SRAM Serial QSPI Evaluation Kit and P-SRAM Parallel Interface Evaluation Kit are available
now. System designers can order these kits at https://www.avalanche-technology.com/contact/evaluation-board-or-kit-request/.

About Avalanche Technology

Avalanche Technology Inc. is the leader in next generation Perpendicular STT-MRAM technology, accepted as the front-runner to replace traditional Flash and SRAM for unified memory architectures in future SOC systems, delivering high performance and low power at 55, 40 and 28nm with scalability to 22 and 14nm. With a proven STT-MRAM portfolio at multiple geometry nodes combined with an intellectual property portfolio of over 300 patents and applications, Avalanche Technology is delivering on the promise of enabling the next generation of scalable embedded unified memory architecture for use in GPUs, MCUs, DSPs, ASSPs and ASICs, making it the true “Next Generation MRAM Company”. For more information, visit us online at http://www.avalanche-technology.com.
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